



Laura Vu

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PRACTICES Intellectual Property, Patent Office Trials, Appellate, Intellectual Property Litigation, Patent Litigation, Patents, AI and Technology, Networking, Software, Federal Circuit Practice

Laura Vu is an associate in the Intellectual Property Practice Group in Haynes Boone's San Francisco office. Her practice focuses on appeals before the U.S. Court of Appeals for the Federal Circuit, federal district court litigation, and post-grant proceedings before the U.S. Patent and Trademark Office.

Drawing on her previous clerkship experience, Laura understands how to identify and develop the most successful arguments in patent disputes to obtain favorable client outcomes. Laura previously served as a law clerk to Judge Raymond T. Chen of the U.S. Court of Appeals for the Federal Circuit and the Patent Trial and Appeal Board.

In district court, Laura has assisted with critical phases of litigation at the pre-trial, trial, and post-trial stage, including motions to dismiss, claim construction, infringement, invalidity, damages, witness preparation and cross-examination, and post-trial motions.

Laura has also appeared in numerous post-grant proceedings before the Patent Trial and Appeal Board representing both petitioner and patent owner. She has drafted briefing at all stages of *inter partes* review proceedings, including petitions and patent owner responses.

Laura's technical experience spans a wide range of technologies, including electric vehicle charging, medical devices, wireless communication, sensor and alarm systems, RFID tracking and antenna design, remote control systems, wellbore telemetry, electronic messaging, and antivirus software. Her background as an embedded software engineer for a university-affiliated research institution and a patent examiner at the USPTO for nearly eight years equips her with the necessary foundational knowledge to successfully represent clients across many diverse technologies.

QUALIFICATIONS

EDUCATION

- J.D., Fordham University School of Law, 2019, magna cum laude
- M.S., Electrical Engineering, Georgia Institute of Technology, 2011
- B.S., Electrical Engineering, Georgia Institute of Technology, 2009, highest honors

CLERKSHIPS

- Judicial Law Clerk to the Honorable Raymond T. Chen, U.S. Court of Appeals for the Federal Circuit, 2022-2023;
- Judicial Law Clerk at the Patent Trial & Appeal Board, 2019-2020

ADMISSIONS

- California
- District of Columbia
- Maryland
- U.S. Patent and Trademark Office

COURT ADMISSIONS

- U.S. Court of Appeals for the Federal Circuit
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PROFESSIONAL AFFILIATIONS AND ENGAGEMENTS

- Federal Circuit Bar Association
- Patent Trial and Appeal Board Bar Association